

2110
MAY 03 2004
PATENT & TRADEMARK OFFICE

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)
FRANCIS ET AL.)
Serial No. 10/008,586) Examiner: T. LEE
Filing Date: NOVEMBER 5, 2001) Art Unit: 2115
For: DEVICE AND METHOD FOR) Attorney Docket No. 53314
SELECTIVELY POWERING DOWN)
INTEGRATED CIRCUIT BLOCKS)
WITHIN A SYSTEM ON CHIP)

STATUS REQUEST

RECEIVED

MAY 06 2004

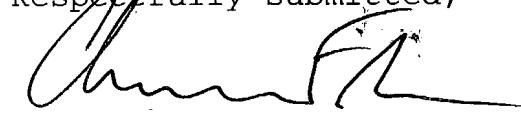
Technology Center 2100

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants request the status of the above identified patent application, which was filed on November 5, 2001. The filing receipt, Notice to File Missing Parts, recorded Assignment and Notice of Publication have been received but there has been no further communication from the U.S. Patent and Trademark Office regarding this application.

Respectfully submitted,



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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 29th day of April, 2004.

